

FIG. 1
(BACKGROUND ART)

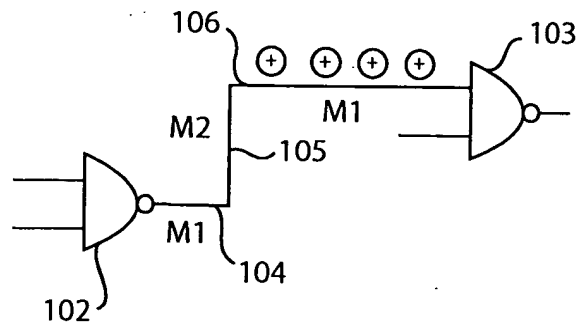


FIG. 2
(BACKGROUND ART)

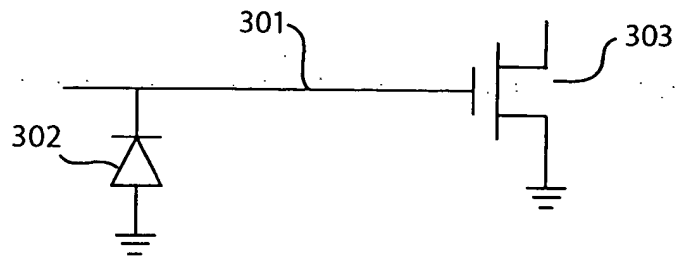


FIG. 3
(BACKGROUND ART)

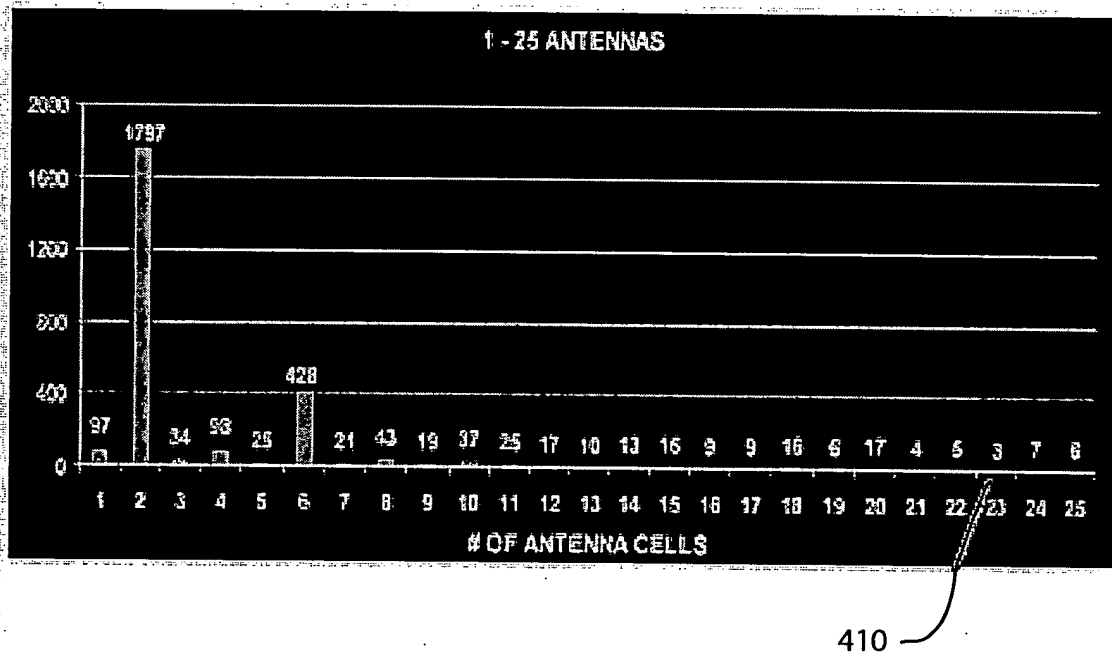


FIG. 4
(BACKGROUND ART)

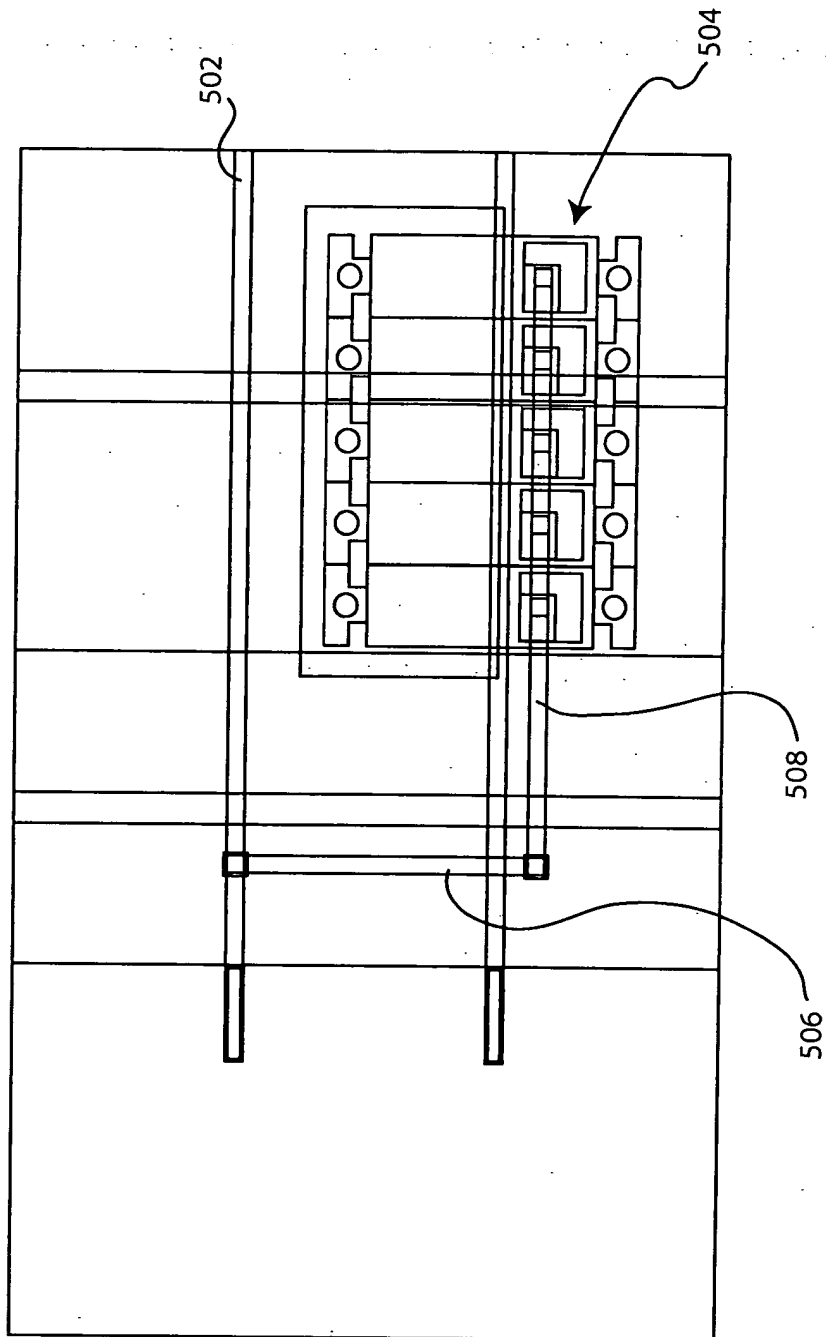


FIG. 5
(BACKGROUND ART)

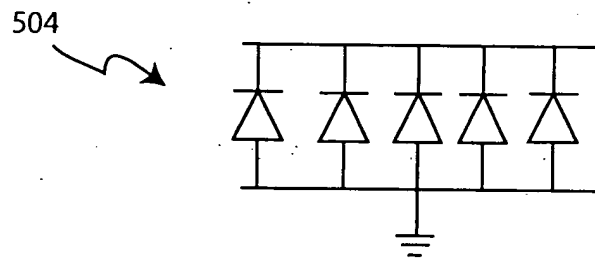


FIG. 6
(BACKGROUND ART)

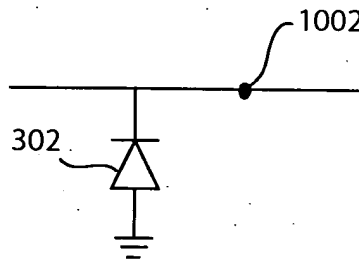


FIG. 10
(BACKGROUND ART)

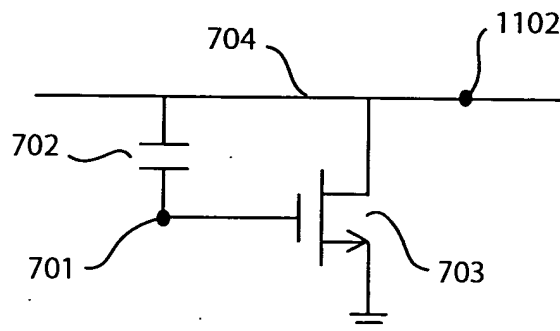


FIG. 11

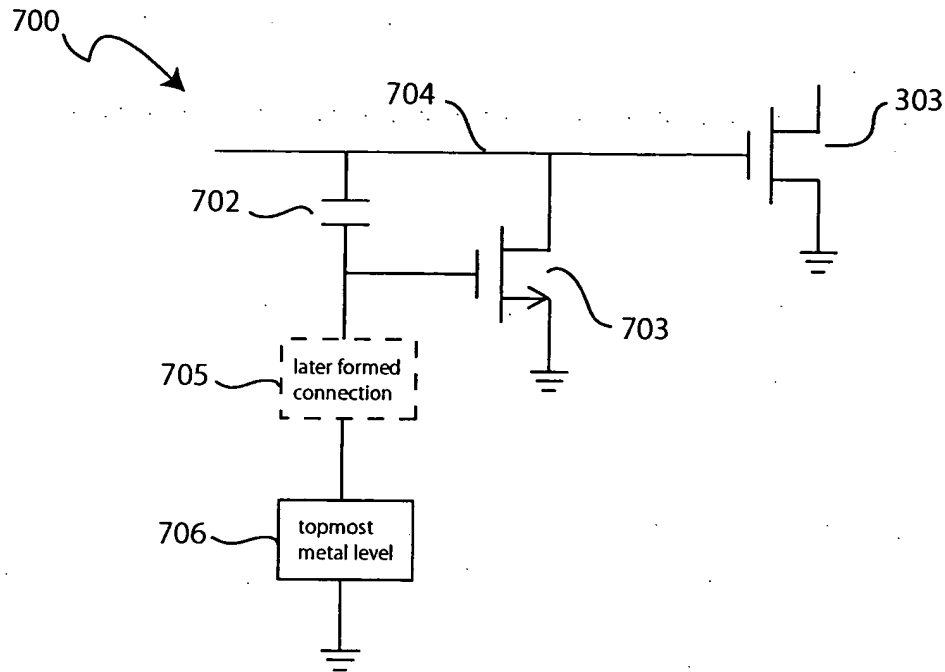


FIG. 7

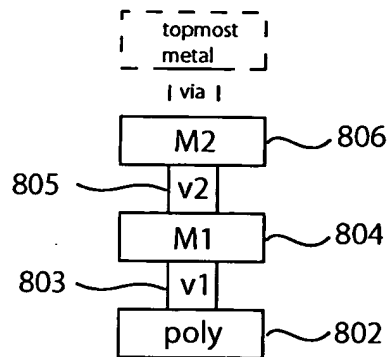


FIG. 8

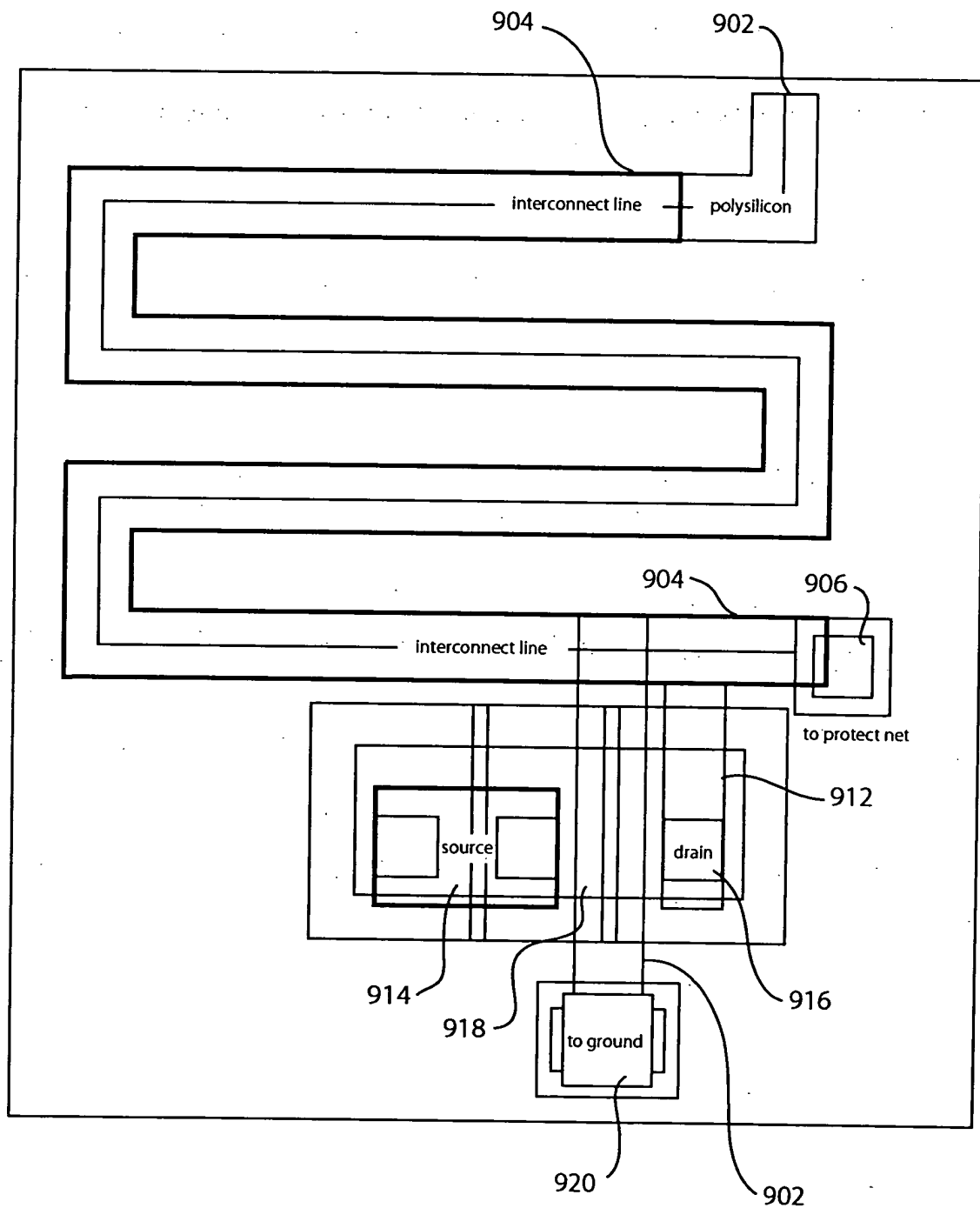


FIG. 9

*Protection Of Integrated Circuit Gates During
Metallization Processes*

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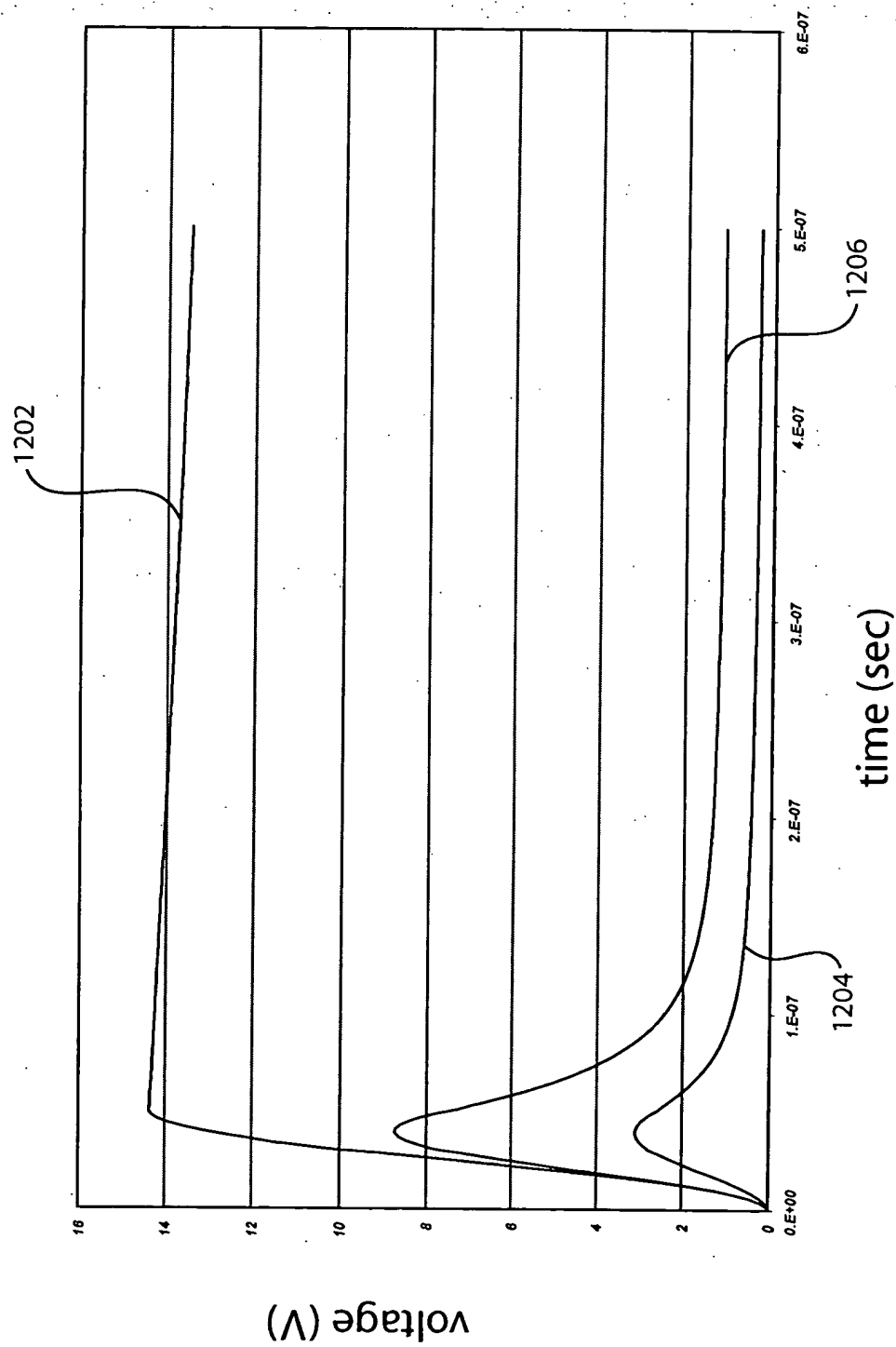


FIG. 12

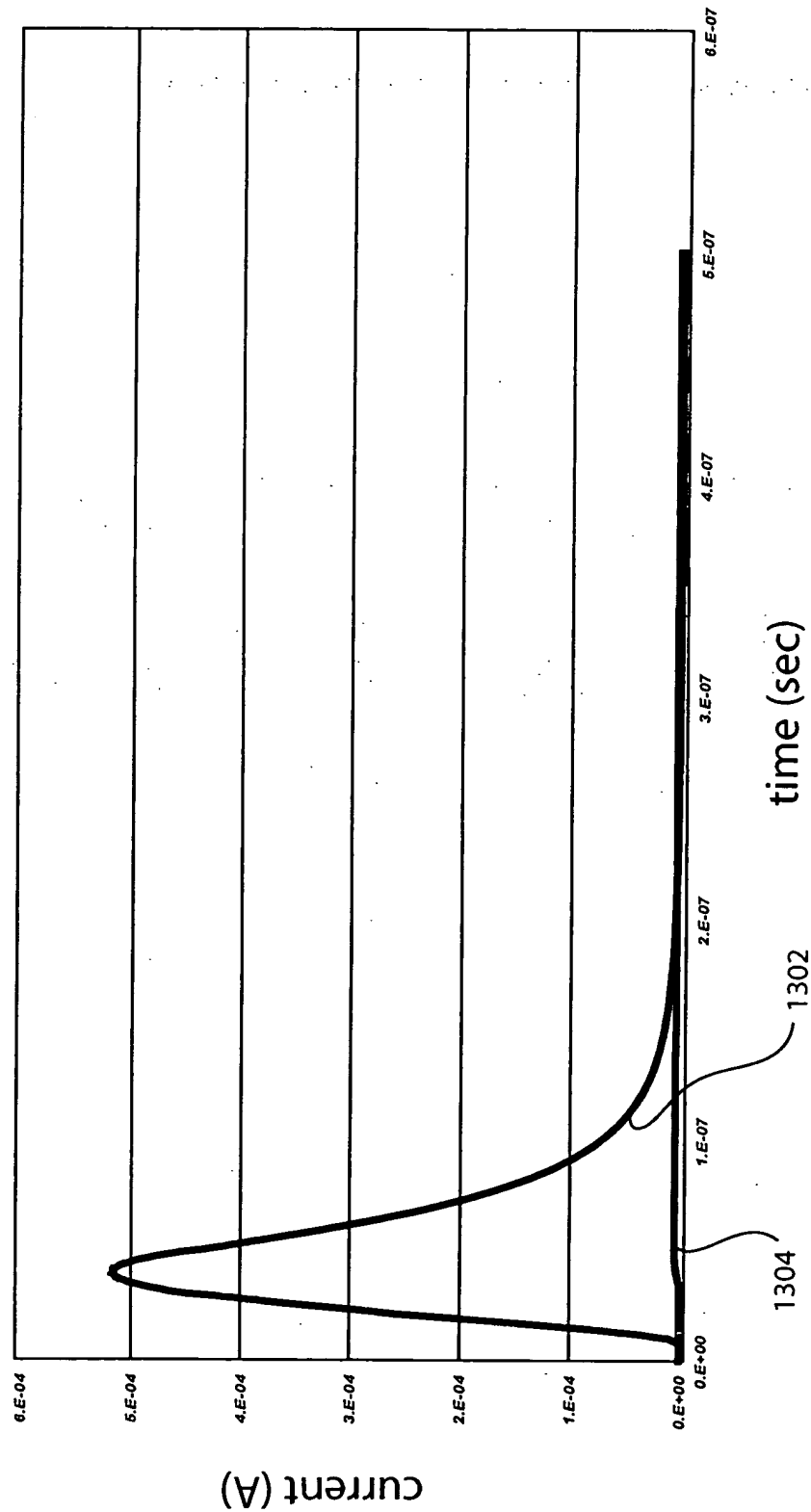


FIG. 13